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PATENT NUMBER and ISSUE DATE

U.S. **UTILITY** Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10092645	03/06/2002	711	122	2185	VITAL

APPLICANTS: Soltis Donald;   2185

CONTINUING DATA VERIFIED:

none   PMV

** FOREIGN APPLICATIONS VERIFIED:**

none   PMV

PG-PUB	DO NOT PUBLISH ☐	RESCIND ☐	

		ATTORNEY DOCKET NO
Foreign priority claimed	☐ yes ☒ no	
35 USC 119 conditions met	☐ yes ☒ no	10016693-1
Verified and Acknowledged Examiner's initials	PMV	

TITLE : System and method for dynamic processor core and cache partitioning on large-scale multithreaded, multiprocessor integrated circuits

U.S. DEPT. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
	Assistant Examiner	**Total Claims**	**Print Claim for O.G**
ISSUE FEE		**DRAWING**	
Amount Due ·	**Date Paid**	**Sheets Drwg.**	**Figs.Drwg.**	**Print Fig.**
	Primary Examiner		
☐ **TERMINAL DISCLAMER**	**PREPARED FOR ISSUE**	**Application Examiner**	

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